

FIG. 1

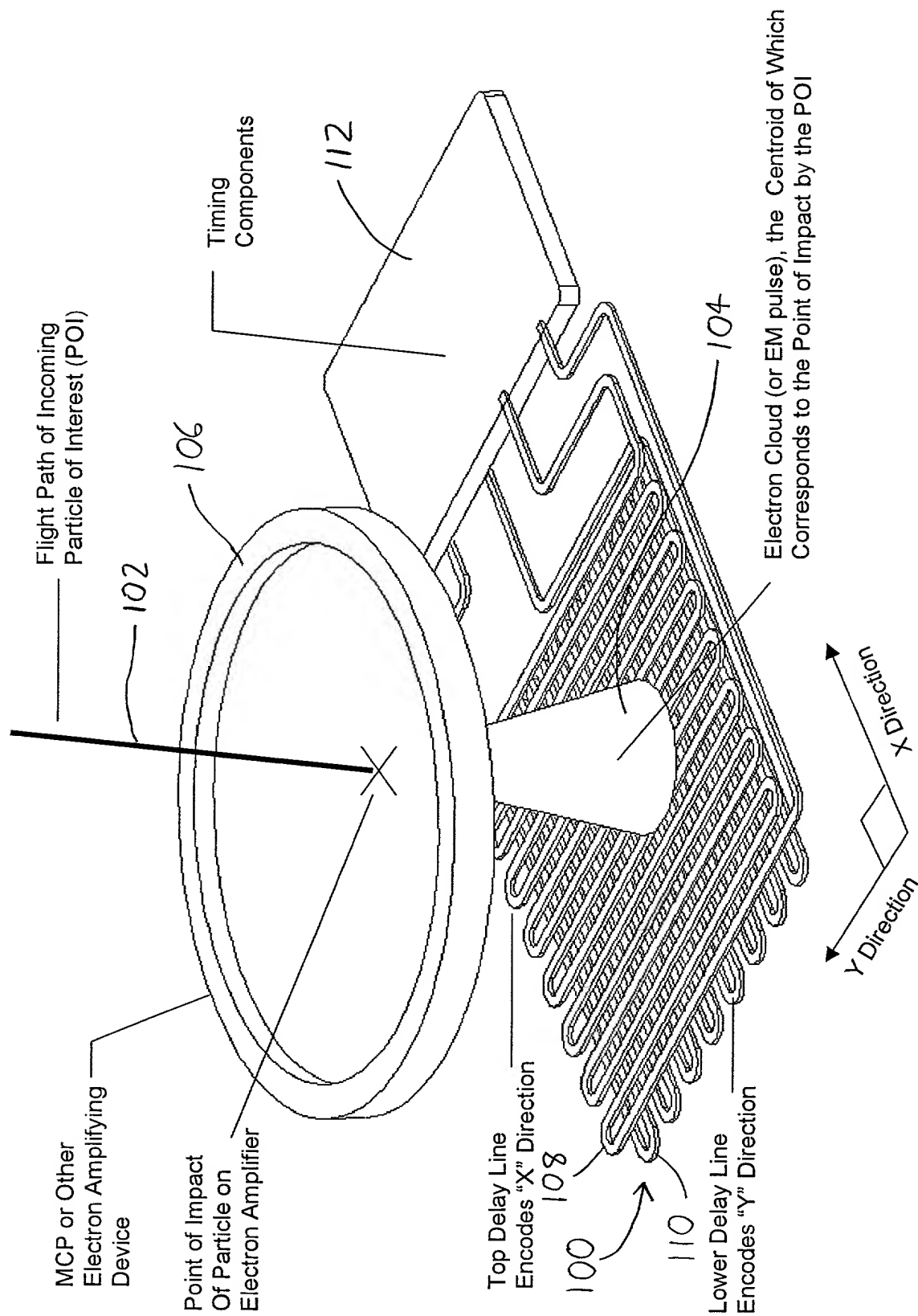


FIG. 2

Friedman et. al.

200

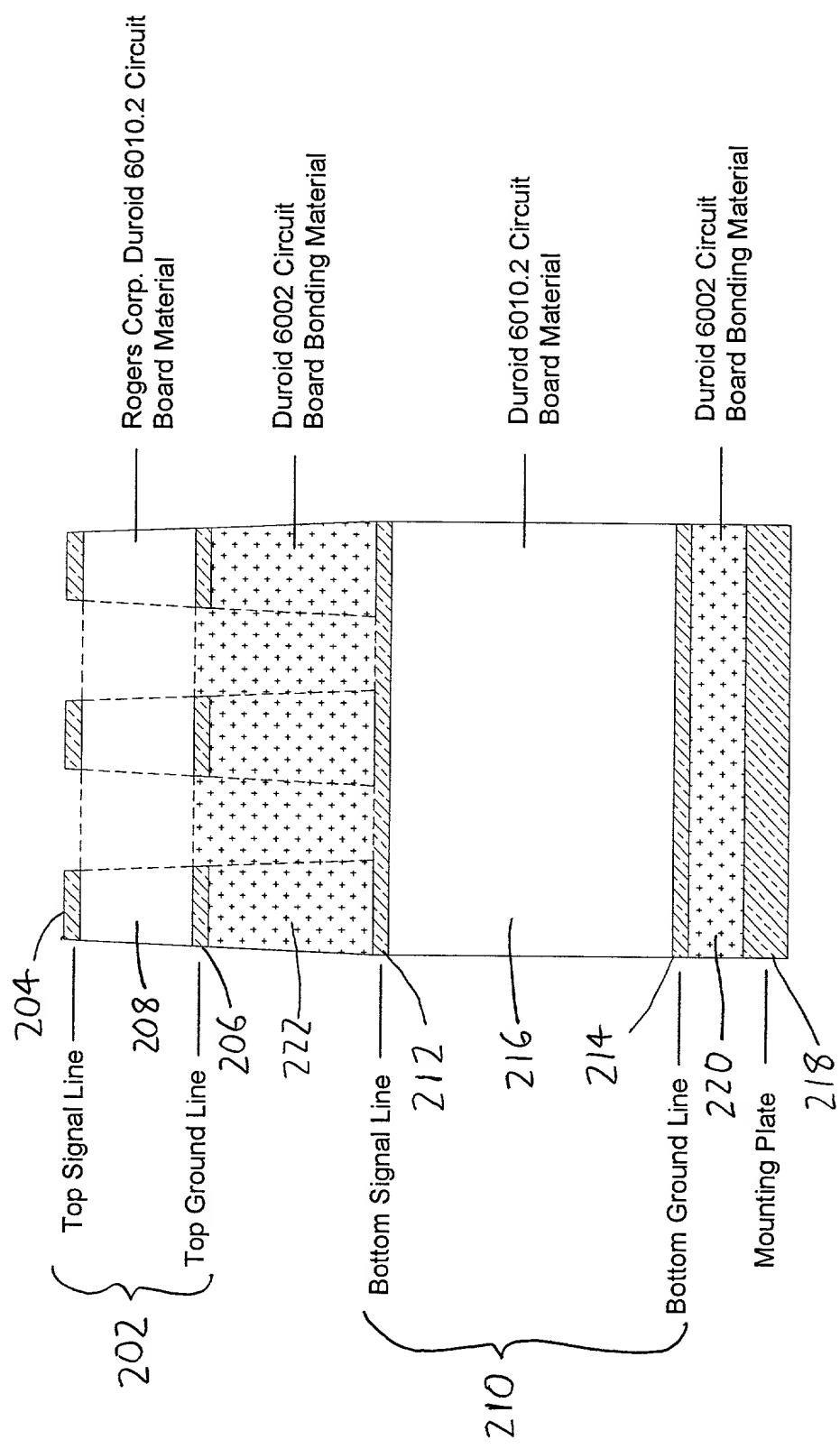


FIG. 3

300

Close Up View of Laminated Delay Line Construction

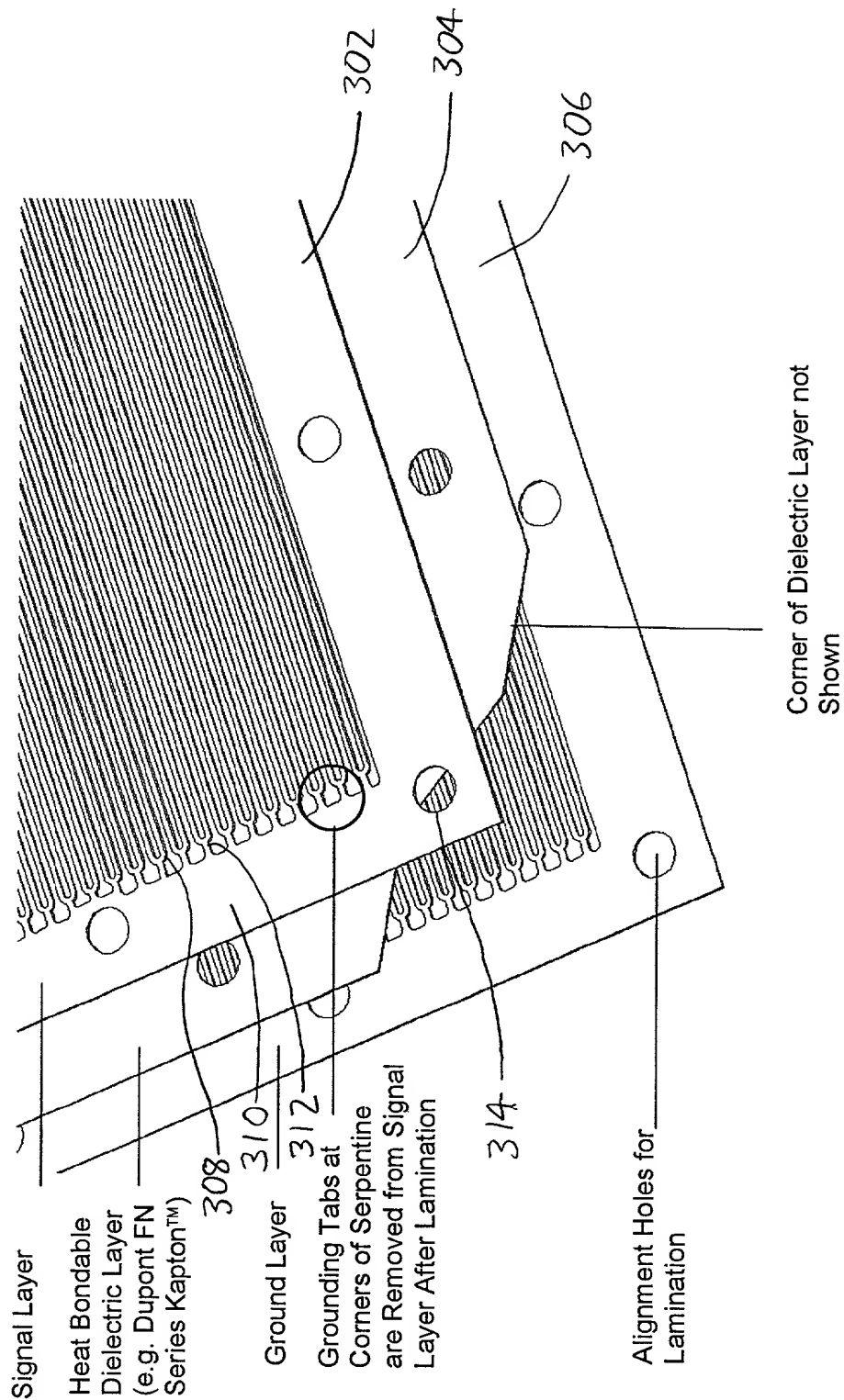


FIG. 4

Cut-Away View of a Delay Line Fabricated via Standard Circuit Board Procedures

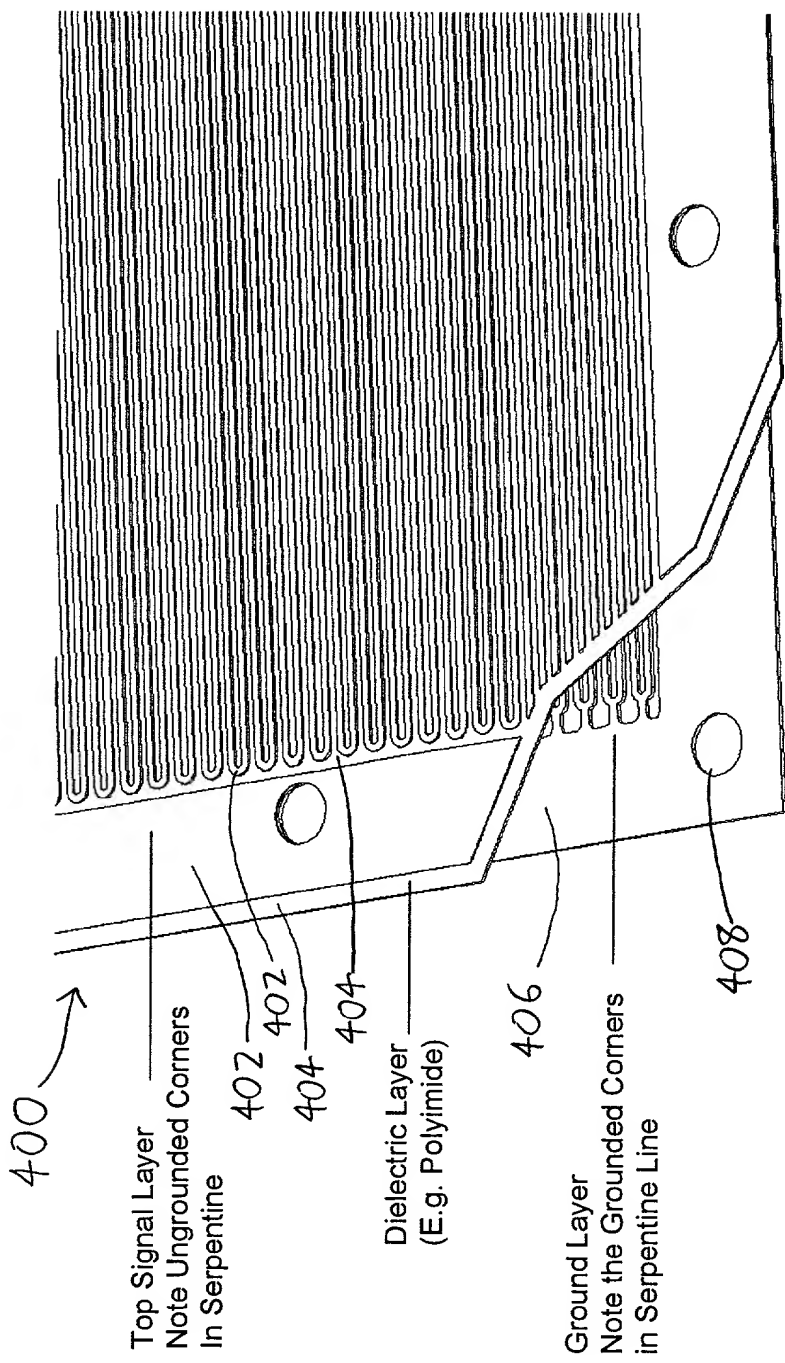


FIG. 5

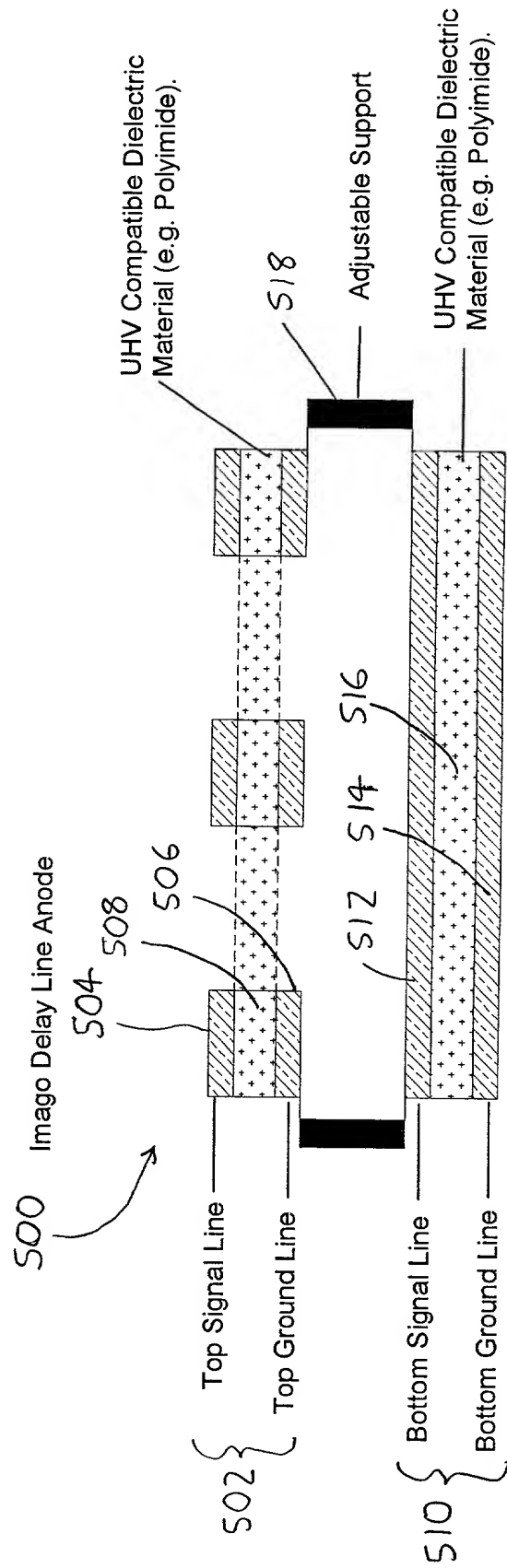


FIG. 6

